

Rockwell DF1

Supported Series: Rockwell MicroLogix 1000, 1100, 1200, 1400, 1500, SLC 5/01, 5/02, 5/03, 5/04, 5/05.

Website: <http://www.ab.com>

Note: Allen-Bradley DF1 driver uses CRC checksum.

HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	Rockwell DF1		
PLC I/F	RS232		
Baud rate	19200	9600, 19200, 38400	
Data bits	8	8	
Parity	None	Even, Odd, None	
Stop bits	1	1	
HMI sta. no.	0		
PLC sta. no.	1	1-31	

PLC Setting:

Communication mode	DF1 Full Duplex protocol 19200, None, 8, 1 (default) Error Check: CRC
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Device Address:

Bit/Word	Device type	Format	Range	Memo
B	I1	DDDdd	0 ~ 25515	Input (I)
B	O0	DDDdd	0 ~ 25515	Output (O)
B	I1n_Bit	SSEEd	0 ~ 303115	SS: slot, EE: sub element, dd:bit
B	O0n_Bit	SSEEd	0 ~ 303115	SS: slot, EE: sub element, dd:bit
B	B3	DDDdd	0 ~ 25515	Bit data file (B3)
B	B10 ~ 13	DDDdd	0 ~ 25515	Bit data file (B10 ~ 13)
B	S_Bit	DDDdd	0 ~ 25515	Status (S) bit level
B	Bfn	FFFDDDDdd	0 ~ 25525515	Bit data file (B3, 10 ~ 254)
B	NfnBit	FFFDDDDdd	0 ~ 25525515	Integer data file bit level (N7, 10 ~ 254)
W	I1n	SSEE	0 ~ 3031	SS: slot, EE: sub element

Bit/Word	Device type	Format	Range	Memo
W	O0n	SSEE	0 ~ 3031	SS: slot, EE: sub element
W	T4SV	DDD	0 ~ 255	Timer Preset Value (T4)
W	T4PV	DDD	0 ~ 255	Timer Accumulator Value (T4)
W	C5SV	DDD	0 ~ 255	Counter Preset Value (C5)
W	C5PV	DDD	0 ~ 255	Counter Accumulator Value (C5)
W	TfnSV	FFFDDD	0 ~ 255255	Timer Preset Value
W	TfnPV	FFFDDD	0 ~ 255255	Timer Accumulator Value
W	CfnSV	FFFDDD	0 ~ 255255	Counter Preset Value
W	CfnPV	FFFDDD	0 ~ 255255	Counter Accumulator Value
W	N7	DDD	0 ~ 255	Integer data file (N7)
W	N10 ~ 15	DDD	0 ~ 255	Integer data file (N10 ~ 15)
W	Nfn	FFFDDD	0 ~ 255255	Integer data file (N7,10 ~ 254)
W	S	DDD	0 ~ 255	Status (S)
W	F8	DDD	0 ~ 255	Floating point data file (F8)
W	Ffn	FFFDDD	0 ~ 255255	
W	Lfn	FFFDDD	0 ~ 255255	
String	STfn	DDD.DDD.DD	0 ~ 255.255.40	File no.Element no.Data no.

Wiring Diagram:

The following is the view from the soldering point of a cable.

9P D-Sub to 8P Mini-DIN: MicroLogix 1000, 1100, 1200, 1400, 1500 (Diagram 1 ~ Diagram 3)



Diagram 1

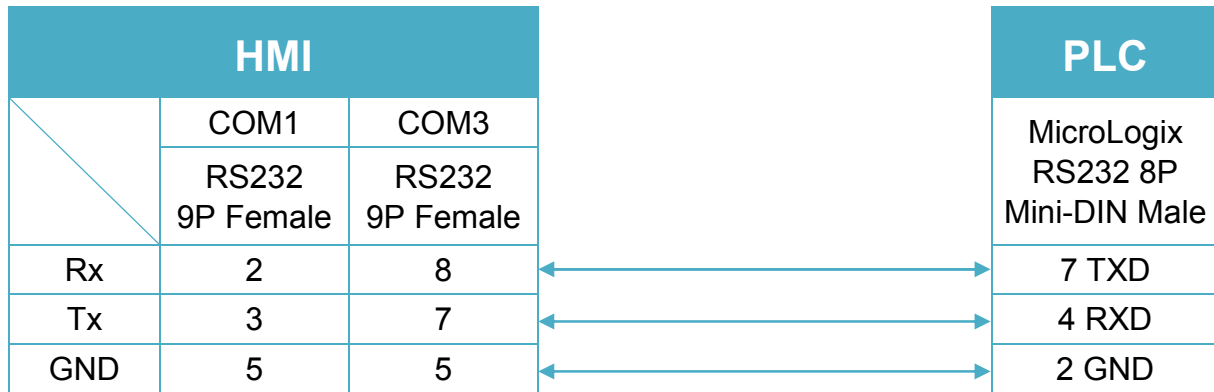
cMT Series **cMT3071 / cMT3072 / cMT3090 / cMT3103 / cMT3151**

eMT Series **eMT3070/ eMT3105 / eMT3120 / eMT3150**

MT-iE **MT8073iE / MT8102iE**

MT-XE **MT8092XE**

MT-iP **MT6103iP/ MT8102iP**


Diagram 2

cMT Series *cMT-SVR / cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*

mTV *mTV*

MT-iE *MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE /
MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE /
MT8101iE / MT8102iE / MT8103iE*

MT-XE *MT8121XE / MT8150XE / MT8090XE*

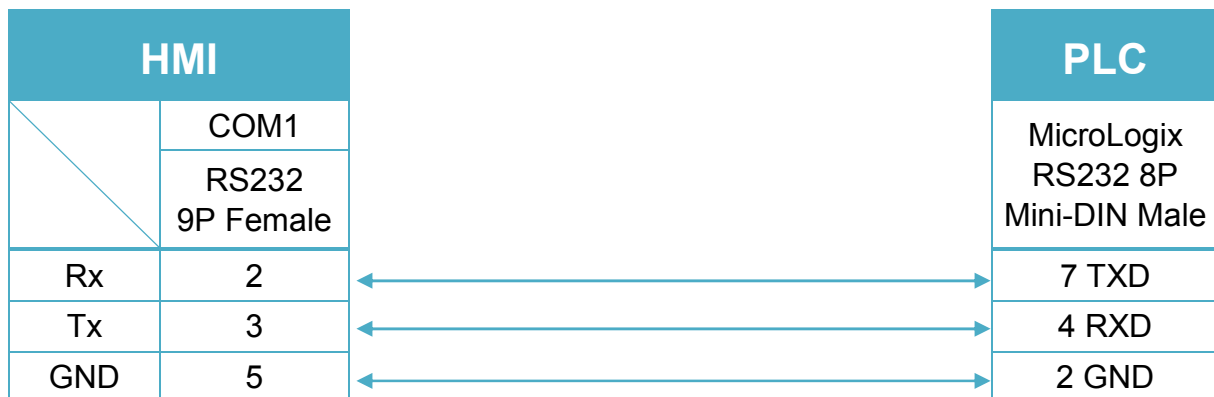
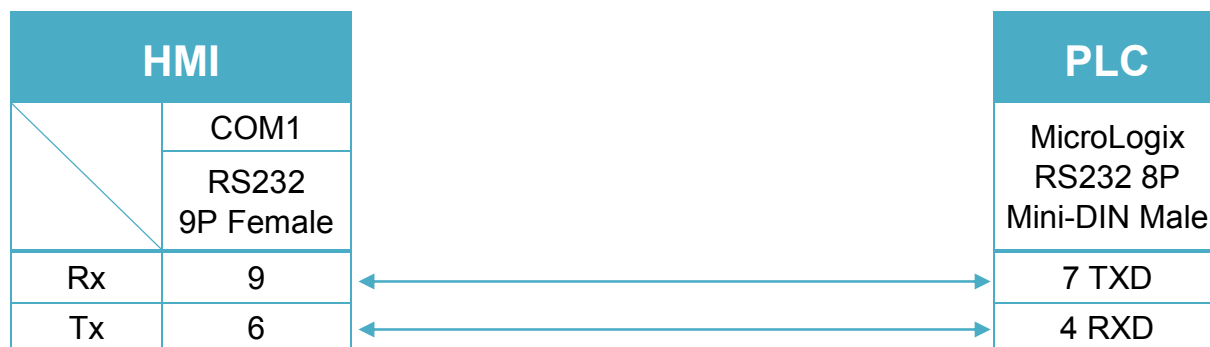


Diagram 3

MT-iE *MT8050iE/ MT8053iE*

MT-iP *MT6051iP / MT8051iP / MT6071iP / MT8071iP*



9P D-Sub to 9P D-Sub: SLC5/03, 04, 05 CH0 (Diagram 4 ~ Diagram 6)

Diagram 4

cMT Series *cMT3071 / cMT3072 / cMT3090 / cMT3103 / cMT3151*

eMT Series *eMT3070/ eMT3105 / eMT3120 / eMT3150*

MT-iE *MT8073iE / MT8102iE*

MT-XE *MT8092XE*

MT-iP *MT6103iP/ MT8102iP*

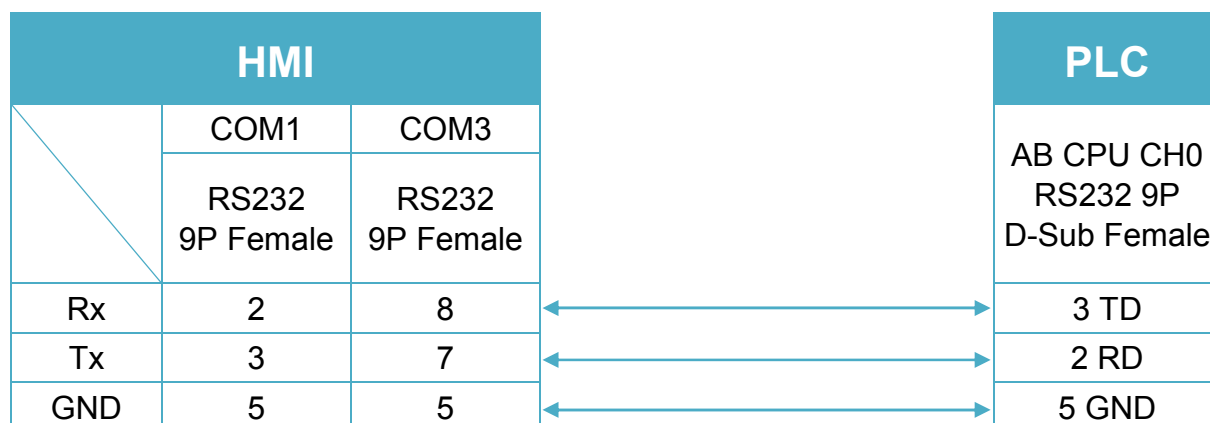


Diagram 5

cMT Series *cMT-SVR/ cMT-G01 / cMT-G02 / cMT-HDM / cMT-FHD*

mTV *mTV*

MT-iE *MT8070iE / MT6070iE / MT8100iE / MT8121iE / MT8150iE /
MT8071iE / MT6071iE / MT8072iE / MT6072iE / MT8073iE /
MT8101iE / MT8102iE / MT8103iE*

MT-XE *MT8121XE / MT8150XE / MT8090XE*

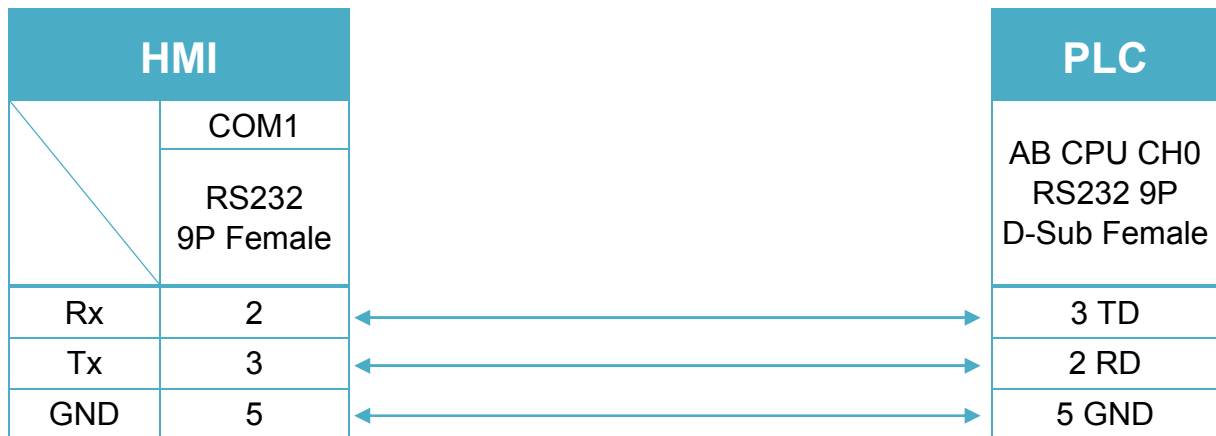


Diagram 6

MT-iE *MT8050iE/ MT8053iE*

MT-iP *MT6051iP / MT8051iP / MT6071iP / MT8071iP*

